



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Greg E. Scott et al.

Title: MEMORY ACCESS CONTROL SYSTEM, APPARATUS, AND METHOD

Docket No.: 884.480US2

Filed: July 28, 2003

Examiner: Mehdi Namazi

Customer No.: 21186

Serial No.: 10/628,772

Due Date: May 24, 2005

Group Art Unit: 2188

Confirmation No.: 5797

Commissioner for Patents

Attn: MAIL STOP ISSUE FEE

P.O. Box 1450

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**Notice of Allowance Date:**

February 24, 2005

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

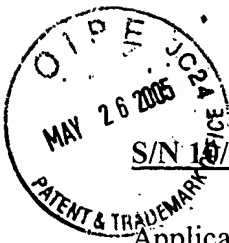
Customer Number: 21186

By Ann M. McCrackin  
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Name

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Signature



S/N 10/628,772

PATENT

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Applicant: Greg E. Scott et al. Examiner: Mehdi Namazi  
Serial No.: 10/628,772 Group Art Unit: 2188  
Filed: July 28, 2003 Docket: 884.480US2  
Title: MEMORY ACCESS CONTROL SYSTEM, APPARATUS, AND METHOD  
Assignee: Intel Corporation Customer No. 21186

COMMUNICATION CONCERNING RELATED APPLICATION(S)

MS Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/902998	July 11, 2001	884.480US1	MEMORY ACCESS CONTROL SYSTEM,
6615329			APPARATUS, AND METHOD

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicant requests that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,  
GREG E. SCOTT ET AL.  
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Date May 24, 2005 By Ann M. McCrackin  
Ann M. McCrackin  
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